

**Amendments to the Claims:**

The following listing of claims replaces all prior versions of the claims:

**Listing of Claims:**

1. (original) A method comprising:

detecting a level on each of a plurality of data channels;  
summing the levels of a subset of data channels to get a summed level, the subset of data channels containing the lowest levels present in the plurality of data channels;  
comparing the summed level with the highest level present in the plurality of data channels; and  
rejecting a common mode interference in each of the plurality of data channels if the summed level exceeds the highest level present in the plurality of data channels.

2. (original) The method of claim 1, where the detecting includes monitoring every data point of the plurality of data channels.

3. (original) The method of claim 1, where the detecting includes detecting a level on each of seven data channels.

4. (original) The method of claim 1, where the detecting includes detecting a pulse on each of a plurality of data channels.

5. (original) The method of claim 1, where the summing the levels of the subset of data channels includes summing the levels of three data channels.
6. (original) The method of claim 5, where the summing the levels of three data channels includes summing the three lowest levels present in the plurality of data channels.
7. (original) The method of claim 1, further comprising multiplying the summed level by a sensitivity factor.
8. (original) The method of claim 7, further comprising adjusting the sensitivity factor to minimize a false alarm rate.
9. (currently amended) A data storage device ~~computer readable medium~~ comprising machine readable instructions for:
- detecting a level on each of a plurality of data channels;
- summing the levels of a subset of data channels to get a summed level, the subset of data channels containing the lowest levels present in the plurality of data channels;
- comparing the summed level with the highest level present in the plurality of data channels; and
- rejecting a common mode interference in each of the plurality of data channels if the summed level exceeds the highest level present in the plurality of data channels.

10. (currently amended) The data storage device computer readable medium of claim 9, where the detecting includes monitoring every data point of the plurality of data channels.

11. (currently amended) The data storage device computer readable medium of claim 9, where the detecting includes detecting a level on each of seven data channels.

12. (currently amended) The data storage device computer readable medium of claim 9, where the detecting includes detecting a pulse on each of a plurality of data channels.

13. (currently amended) The data storage device computer readable medium of claim 9, where the summing the levels of the subset of data channels includes summing the levels of three data channels.

14. (currently amended) The data storage device computer readable medium of claim 9, where the summing the levels of three data channels includes summing the three lowest levels present in the plurality of data channels.

15. (currently amended) The data storage device computer readable medium of claim 9, further comprising machine readable instructions for multiplying the summed level by a sensitivity factor.

16. (currently amended) The ~~data storage device computer readable medium~~ of claim 15, further comprising machine readable instructions for adjusting the sensitivity factor to minimize a false alarm rate.

17. (original) An apparatus comprising:  
a plurality of data channels;  
an array of input filters coupled to the plurality of data channels;  
a pulse detection circuit coupled to the array of input filters;  
a common mode rejection circuit coupled to the pulse detection circuit, the common mode rejection circuit operable to:  
sum the levels of a subset of data channels to get a summed level, the subset of data channels containing the lowest levels present in the plurality of data channels,  
compare the summed level with the highest level present in the plurality of data channels, and  
reject a common mode interference in each of the plurality of data channels if the summed level exceeds the highest level present in the plurality of data channels; and  
a pulse data queuing and transmission circuit coupled to the common mode rejection circuit.

18. (original) The apparatus of claim 17, further comprising a guidance circuit coupled to the pulse data queuing and transmission circuit.